**WEEKLY PROGRESS REPORT**

**Sightline Capstone Project**

**Week 16**

**27 April 2019**

**Tai:**

**This week:**

**Next week:**

**Question:**

**Kimball:**

**This Week:**

* Issues were found with the board layout design by the Sightline team. There are problems with translation from Eagle to Altium, which doesn’t affect the design but does create some confusion.
* Although design rules were downloaded from Sunstone manufacturer and the board passed DRC with no errors in Eagle, Sightline found some issues with the board design using Altium DRC and from visual inspection of the Eagle design.
* One of the issues has been isolated to clearance < 6 mil between same signal traces/vias not creating an error when the DRC is ran in Eagle.
* All parts have been approved and are available/in stock on the current BOM

**Next Week:**

* Meeting first thing Monday at Sigthline to fix issues with board design and finalize gerber files for manufacturing
* Work on the following final documentation:
  + Test Plan for SLA1500 CAM
  + Design Report
  + Power Point Presentation
  + Poster Design

**Comments/Questions:**

None at this time